

**Today's paper cs302 – Digital Logic Design****Total Questions: 52****Total Marks: 80****Questions & Marks Division:-****40 MCQs\*1 Mark, 4\*2 Marks, 4\*3 Marks, 4\*5 Marks.****Question 41:-** Explain state assignment process. (2)**Question 42:-** How many bytes will be in  $16K \times 8$  memory? (2)**Question 43:-** Explain erase operation in context of FLASH Memory. (2)**Question 44:-** Which of the following is DIGITAL & which is ANALOG? (2)

- a) Pressure in a bike's tire
- b) Temperature
- c) Speed of a car
- d) Number of students in a class

**Question 45:-** Define "excitation inputs". (3)**Question 46:-** Explain rotate Right Operation of shift register with the help of diagram. (3)**Question 47:-** Divide the following binary number "1101" by the binary number "101". Write all the steps. (3)**Question 48:-** What are the three operations of FLASH MEMORY? (3)**Question 49:-** Convert the following caveman number system into decimal number system. (5)

- i)  $\Omega \uparrow \Sigma$
- ii)  $\uparrow \Delta \Delta \Sigma$

**Question 50:-** Differentiate between "MEMORY CPACITY" & "MEMORY DENSITY". (5)**Question 51:-** Draw next-state table of an UP-Counter with help of J-K flip-flop. (5)**Question 52:-** Explain FLASH Analogue-to-Digital converter. (5)