AOA hi every one

cs302 my today`s paper

write down atleast 2 functions of a shift regiser?
Explain memory select or enable signals
Explain the implementation of FIFO memory by using Ram.?
what is meant by Monotonicity of Digital to Analog converter?
Given the following statement used in PLD programming:

Y PIN 23 ISTYPE „com";
Explain what does this statement mean?
aur ek diagrame thi jis ki input next state aur output lihkna tha?
aur boolean expression k ek swal tha (A+B) = (A+B`)
(A+B)` = (A`+B`).
Write the great decimal numer .
1 8 bit decimal number
10 bit decimal number
aur ek tabe tha uss ki samaj nahi ayi thi aur 2 yaad nahi ab.
Hour counter kia hai aur iss ki tyes wala tha.
mostly mcs`q pata nahi kahan se thy liken past papers mn se nahi thy.

plz pray for me.
REMEMBER ME IN YOUR PRAYERS .

CS302 16 July 2012 Current Paper
Mcques were mostly new some belongs to past paper.
FIFO memory Implementation (5 Marks)
Some binary values or number that were for converting into the Hexa decimal number (5 Marks)
2 Application of ROM (5 or 3 Marks)
Name of memory operstions (2 Marks)
In the highest frequency component in an analog signal is 20 KHz, what is the minimum sample frequency (2 Marks)

Q:Difference b/w mooore and mealy
Q:what will be effect on cobinational logic circuit using nand gate?
Q:how many Byt in 32k x 8
Q:monotonicity of digital to analog convrter
Q: excitation inputs
Q: 2 input 4 mux table
Q fifo implemented by RAM
Q: Describe counter using decoding circut/
Baki yad nahi M.C.Qz mostly 1 to 30 lec

Today paper (total 40 mcq's and 4 2marks Ques 4 3marks ques and 4 5marks ques)
3. Differentiate between Moore machine and Mealy machine. (Marks 2)
Answer:- Page 318
The Sequential circuit whose output depends on the current state and the input is known as Mealy Machine. Sequential circuit whose output is determined by the current state only is known as Moore Machine.
Q No. 6 How can calculate the frequency of an unknown signal?
Name three Operations that can be performed on FLASH Memory
2. Explain Rotate Right Operation of shift register with the help of Diagram. (Marks 3)
Question No: (Marks: 5)
Explain Memory Select or Enable Signals
Explain application of demultiplexer
3: Explain the next-state table with the help of a table for any sequential circuit?
and some new question

Q1:Differentiate between Moore machine and Mealy machine.
Q2:Explain Memory Select or Enable Signals.
Q3:explain flash analogue to digital converter(5 marks)
Q4;differentiate state assignment and state reduced assignment.
Q5:three characteristic of serial in/serial out
Q6:truth table of S-R laches.
Q7;differentiate b/t PROMS and ROM.

my today`s current paper
explain state assignment.
name two types of d/a converters.
why a two bit comparator called parellel comparator?
nand base s-r latch.
next state table of 3 bit up counter
multiplexing in DRAM
difference truth table and next state table.

CS302 FINAL EXAM
MY TODAY'S PAPER
16 JULY 2012
TOTAL 52 QUESTIONS
40 MCQS
4 QUESTIONS 2 MARKS
4 QUESTIONS 3 MARKS
4 QUESTIONS 5 MARKS

Most of the mcqs were from past papers…..

Which of the following are analog quantities, and which are digital?

a) Pressure in a bicycle tire
b) Number of students in a class
c) Speed of a car
d) Temperature ……. 2 marks
……..
A 4-bit serial in/parallel out register contains the value "0100", how many clock signals will be required to shift the complete data out of the register. 2 marks
…….
What is meant by Monotonicity of D/A convertors? ……. 2 marks

…….
Why some counters are called present counters? ….. 2 marks

……
Difference between Johnson and ring counter? 3 marks

…….
What is memory expansion process …… 3 marks
……

What decimal numbers are represented by each BCD code?
(a) 10001001
(b) 001001111000
© 000101010111 …… 3 marks

……..
The following table gives the inputs/outputs sequence derived from a state diagram. Use this information to draw the state diagram. You can show the inputs/outputs (when switching to next state) with directed arrows. …… 3 marks

state
A

B
B
C
a
Input
1
0
1
0

Output
0
1
1
0

……
Draw the function table of 8-t0-3 encoder ….5 marks
……

Answer the following questions:
a) What is difference between following two statements:
X := D;
Y = D;

b) Why do we use dot extension ".CLK" (e.g. X.CLK = Clock) with logical declaration statements? ……. 5 marks
……..
One more question of 5 marks …… diagram was given we were asked to make to input and output table
Answer:
state
a
f
d
c
a
b
c
f
d
c
input
0
0

0
0
1
1
1
0
0

output
0
0
1
0
0
1
0
0
1